

# MICROPROCESSOR SYSTEM WITH SOFTWARE EMULATION PROCESSED BY AUXILIARY HARDWARE

## Abstract

A microprocessor system capable of software debug includes a host computer for executing remote debug, and a program memory for storing a monitor program for providing monitoring of the host computer and a user program. At least one break point address holder temporarily stores a break point address from the host computer. A break point comparator unit is connected to the break point address holder for comparing the break point address from the break point address holder with an address of the user program being executed, and for outputting an interrupt control signal when the addresses match. A controller controls the break point comparator unit, and a microprocessor is electrically connected to the host computer.